



Japan Patent Office



WORKSHOP

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LIST OF SPEAKERS

prepared by the International Bureau of WIPO

IAN MORRISH

Mr. Ian Morrish has been an European Patent Examiner at European Patent Office (EPO) since 2003. He obtained Master Degree of Electrical Engineering at University of Southampton.

Mr. Morrish has served in various departments at EPO: Search and substantive examination of patent applications (European and PCT) in the field of cluster electronics and technology; Digital to analog conversion, lighting control systems and aircraft control systems; and Examination of oppositions filed against granted European patents Chairing of oral proceedings in examination and opposition.

Mr. Morrish served as Consultant Engineer, testing digital flight controls for M346, military trainer jet, at Aermacchi, Varese, Italy from 2001 to 2003.

Mr. Morrish was a Team Leader and Department Head for the design and test for flight controls of 728 civil aircraft, at Fairchild Dornier, Munich during 2000 to 2002.

Mr. Morrish was also a System Design Engineer for system design for BR710 jet engine controllers, at BMW-Rolls Royce, Berlin, Germany, from 1997 to 2000.

Mr. Morrish served at Flight Test at Gulfstream Airspace, Savannah, USA, responsible for coordinating test results/problems and requirements between Gulfstream and Smiths Industries from 1995 to 1996.

Mr. Morrish was an Electronics Design Engineer for design of electronic fuel control systems for Airbus A320 and design of digital engine control systems for Gulfstream GV, at Smiths Industries, Basingstoke, England, from 1991 to 1995.

TARO DAIKO

Mr. Daiko is a Deputy Director, Information Dissemination and Policy Promotion Division, General Affairs Department, Japan Patent Office (JPO).

Mr. Daiko joined the JPO in April, 2004 as an Assistant Examiner. He became Deputy Director of the Patent Information Policy Planning Office of the International Dissemination and Policy Promotion Division in July, 2012. Mr. Daiko and his team are involved in planning patent information dissemination policy. He is planning the service of IPDL (The Industrial Property Digital Library), the public service of patent information; the service of AIPN(Advanced Industrial Property Network), the service of provision of patent examination information in JPO; and for PAJ (Patent Abstracts of Japan).

Mr. Daiko obtained Master's Degree in Engineering from Osaka University, Japan, in March 2004.

DAISUKE KAWAMURA

Mr. Kawamura is a Deputy Director, Quality Management Office, Japan Patent Office (JPO).

Mr. Kawamura joined the JPO in April, 2001 as an Assistant Examiner. He became Deputy Director of the Quality Management Office of the Administrative Affairs Division in October, 2012. Mr. Kawamura and his team are involved in planning measures to improve the quality of search and examination. In addition, in his tenure from February 2007 to June 2008 in the

International Affairs Division, he was a chief of the European section in intellectual property related issues. In this capacity, he was primarily engaged in the cooperation activities of the Trilateral Patent Offices (United States Patent and Trademark Office, European Patent Office, and the JPO).

Mr. Kawamura obtained a Master's Degree in Materials Science and Engineering from Tokyo Institute of Technology, Japan, in March 2001.

TATSUYA TADA

Mr. Tada is a Deputy Director, Examination Standards Office, Japan Patent Office (JPO).

Mr. Tada joined the JPO in April, 2000 as an Assistant Examiner. He became Deputy Director of the Examination Standards Office of the Administrative Affairs Division in January, 2012. Mr. Tada and his team are involved in reviewing and amending examination guidelines and practices for patent examination including the search and examination in international phase of the PCT as well as projects of comparative study of patent examination practices between other patent offices.

Furthermore, Mr. Tada has examined more than 1,500 national patent applications and provided more than 150 International Search Reports during career.

Mr. Tada obtained Bachelor and Master Degree in Science from Tokyo University, Japan, in March 1998 and 2000, and L.L.M. Degree from the Munich IP Law Center, jointly run by the Max Planck Institute for Intellectual Property and Competition Law, the University of Augsburg, the Technische Universität München, and The George Washington University Law School in Washington, D.C. (USA).

KIYOSHI AOSHIKA

Mr. Aoshika is an Assistant Director, Policy Planning and Research Section, Administrative Affairs Division, First Patent Examination Department, Japan Patent Office (JPO).

Mr. Kiyoshi Aoshika joined the Japan Patent Office in April, 2007 as an Assistant Examiner. He became an Examiner in Semiconductor Division at the Third Patent Examination Department (at that time) in April, 2011. Since October, 2012, Mr. Kiyoshi Aoshika has been involved in IP5/WG3 (Examination Practice), IP5/StatisticalWG. Furthermore, Mr. Kiyoshi Aoshika has a great deal of experience training foreign patent examiners.

YU KOCHI

Mr. Kochi is an Assistant Director, Examination Promotion Office, Japan Patent Office (JPO).

Mr. Kochi joined the JPO in April, 2004 as an Assistant Examiner. He promoted to an Examiner in April, 2007. He had been experienced business machine division and distance measurement division as an examiner.

He was transferred to Examination Promotion office in April, 2012. He is mainly in charge of outsourcing concerning patent classification.

Mr. Kochi obtained a Master's Degree in Science from Kyushu University, Japan, in March 2002.

MASATAKA SAITO

Mr. Saito is an Assistant Director, Examination Standards Office, Japan Patent Office (JPO).

Mr. Saito joined the JPO in April, 2007 as an Assistant Examiner. He became an Assistant Director of the Examination Standards Office of the Administrative Affairs Division in April, 2012.

Mr. Saito is involved in maintaining and improving the internal material for Japan Patent Office's examiners to establish the international search reports and the international preliminary examination reports.

Mr. Saito obtained Bachelor of Degree of Engineering in March 2005 and Master Degree of Computer Science in March 2007, from Tokyo Institute of Technology, Japan.

TAKASHI YAMASHITA

Mr. Yamashita is a Director of PCT International Cooperation Division, Innovation and Technology Sector in World Intellectual Property Organization (WIPO). He obtained Master Degree of Science at Tokyo University in 1988.

He joined JPO in 1988, and became a patent examiner in 1992. During his varieties of career on intellectual property, notably he has been involved in international affaires and negotiations, such as patent harmonization (WIPO), and TRIPS (WTO). From 1997-1998 he worked for Ministry of Foreign Affairs as a Deputy Director, and from 2000 to 2003 he also worked for the Permanent Mission of Japan in Geneva as a First Secretary. From 2006-2008 he participated in various international for a, such as WIPO, WTO, CBD, WHO, Trilateral meetings as a Director of Multilateral Policy Office, International Affairs Division, JPO.

In 2008, he moved to the patent examination division as a managing position while he got also involved in PCT issues as a Director of PCT Reform Office in JPO and participated in meetings such as WIPO working group and Trilateral meetings. Since July 2010, he has been working in the current position in WIPO.

KEN-ICHIRO NATSUME

Mr. Natsume, a national of Japan, has been the head of WIPO Japan Office since January 2012.

Mr. Natsume joined the Japan Patent Office (JPO) in 1992 as a patent examiner. During this period, he became a visiting associate at the California Institute of Technology, USA, from 1996 to 1997. From 2000 to 2001, he was with the Technology Research Division of JPO, dealing with various intellectual property issues including cutting edge topics such as business method patents and biotechnology related patents.

Mr. Natsume was posted in the First International Affairs Division of Ministry of Foreign Affairs of Japan (MOFA), from 2001 to 2003, where he covered international intellectual property issues including industrial property, copyright and plant variety. During his service at MOFA, he was engaged in Japan's accession of WIPO Performances and Phonograms Treaty (WPPT).

During his service at the Examination Standard Division of the JPO from 2004 to 2005, Mr. Natsume was engaged in releasing several new examination standards of patents and utility models. He was also an appeal examiner at the JPO from 2009 to 2010 in the field of electronic image devices.

Mr. Natsume was also at the Permanent Mission of Japan to the International Organization in Geneva, from 2006 to 2009, where he was involved in various international intellectual property discussions and debates such as at WIPO, WTO/TRIPS and WHO.

Mr. Natsume was a director for multilateral policy division of JPO from 2010 to 2011 and played a leading role in the international discussions and negotiations including WIPO, WTO/TRIPS, WHO, CBD, Trilateral Meetings and IP5 Meetings as well as bilateral talks and negotiations with European and North American partners such as EU and USA.

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